Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

MAR 2 2 2002
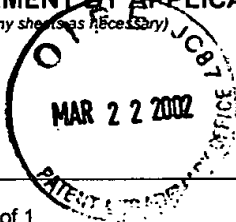
Sheet 1 of 1

Complete if Known	
Application Number	08/902133
Filing Date	July 29, 1997
First Named Inventor	Forbes, Leonard
Group Art Unit	2815
Examiner Name	Eckert II, George

Attorney Docket No: 00303.356US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
[init]	US-5698869	12/16/1997	Yoshimi, M , et al	257	192	09/13/1995
[init]	US-5886376	03/23/1999	Acovic, A. , et al	257	316	07/01/1996
[init]	US-5976926	11/02/1999	Wu, J. Z., et al	438	237	10/10/1997
[init]	US-6031263	02/01/2000	Forbes, , et al	257	315	07/29/1997
[init]	US-6084248	07/04/2000	Inoue, S.	257	66	02/03/1998
[init]	US-6093937	07/25/2000	Yamazaki, S. , et al	257	59	02/19/1997
[init]	US-6099574	08/08/2000	Fukuda, S. , et al	703	14	12/16/1997
[init]	US-6130147	10/10/2000	Major, J.S. , et al	438	604	03/18/1997
[init]	US-6166401	12/26/2000	Forbes, L.	257	77	08/20/1998
[init]	US-6271566	08/07/2001	Tsuchiaki, M.	257	347	11/16/1999

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No [1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
[init]		BURNS, S.G. ,et al. , In: Principles of Electronic Circuits, West Publishing Company, St. Paul, MN, (1987), pp.382-383	
[init]		WOLF, "Semiconductor Memory Process Integration", Silicon Processing for the VLSI Era Vol. 2: Process Integration, Lattice Press, California,(1990), pp.623-626	

EXAMINER	*[signature]*	DATE CONSIDERED	6/19/02